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| Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i> | | | | Application Number | | Filed Herewith | | | |
| | | | | Filing Date | | September 5, 2003 | | | |
| | | | | First Named Inventor: | | Shashi B. Sakhuja | | | |
| | | | | Examiner name: unknown | | GROUP: unknown | | | |
| Sheet | 1 | of | 1 | Attorney Docket Number | | CAT-00001 | | | |
| U.S. PUBLISHED PATENT APPLICATIONS | | | | | | | | | |
| Examiner Initials* | Cited No. ¹ | U.S. Patent Document Number Kind Code ² <i>(if known)</i> | | Name of Patentee or Applicant of Cited Document | | Date of Publication of Cited Document MM-DD-YYYY | | Pages, Columns, Lines Where Relevant Info. Appear | |
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| U.S. PATENT DOCUMENTS | | | | | | | | | |
| Examiner Initials* | Cited No. ¹ | U.S. Patent Document Number Kind Code ² <i>(if known)</i> | | Name of Patentee or Applicant of Cited Document | | Date of Publication of Cited Document MM-DD-YYYY | | Pages, Columns, Lines Where Relevant Info. Appear | |
| | A01 | 5495453 | | Wociechowski et al. | | 02/27/1996 | | | |
| | A02 | 5901085 | | Kramer et al. | | 05/04/1999 | | | |
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| FOREIGN PATENT DOCUMENTS | | | | | | | | | |
| Examiner Initials* | Cited No. ¹ | Foreign Patent Document Office ³ Number ⁴ Kind Code ⁵ <i>(if known)</i> | | | Name of Patentee or Applicant of Cited Document | | Date of Publication of Cited Document MM-DD-YYYY | Pages, Columns, Lines Where Relevant Info. Appear | T ⁶ |
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| OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) | | | | | | | | | |
| Harrison et al.: "A CMOS :Programmable Analog Memory-Cell Array Using Floating Gate Circuits"; IEEE 2001; IEEE Transactions On Circuits And Systems-II: Analog And Digital Signal Processing, Vol. 48, No. 1, January 2001. pp. 4-11. | | | | | | | | | |
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| EXAMINER | | | | | DATE CONSIDERED | | | | |

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